

CONFERENCE VENUE

Montreux is located on the north shore of scenic Lac Léman (Lake Geneva), a vibrant, centrally located and easily accessible area that includes the cities of Geneva, Lausanne, Montreux and Vevey. The larger area of western and central Switzerland also incorporates the important cities of Neuchâtel and Bern. The entire region has a rich background as a cultural and commercial hub with international recognition, and very close links with the industrial centers in eastern France, northern Italy, and southern Germany. The Geneva International Airport provides easy access to the Montreux-Vevey area from all over the world, while efficient train service as well as scenic highways offer reliable links to all metropolitan centers in south-central Europe. There is also direct, fast and frequent train connection between Geneva International Airport (GVA) and Montreux; the travel time between Geneva and Montreux is about one hour.

Montreux Convention Center

Conveniently located on the shores of Lake Geneva, and within easy walking distance to most of the hotels in the area, the Montreux Convention and Exhibition Center offers two large conference halls, numerous rooms for parallel technical sessions, and extensive support facilities. Traditional home of the world-famous Montreux Jazz Festival, the convention center provides the ideal venue for the event.

Montreux will be your home port for journeys of discovery through the canton of Vaud, the Lake Geneva Region, nearby France, or places which are famous for their history, their picturesque sites, their food and wine, or all of these and more. A suggestion to start with: the vineyards east and west of Lausanne are traversed high and low by pleasant footpaths, and in many of the vintners' villages – especially at weekends – cellars for wine tasting are open to the public.

In the heart of the country you are quite likely to find a centuries-old castle or fortified village – see Oron or Gruyères, for example. Geneva and Bern are fascinating blends of old and new, while further afield, in Interlaken or Lucerne, Zermatt or Chamonix, nearly every street has a mountain view.



Château de Chillon, Montreux

CONTACT INFORMATION

Conference Secretariat

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ORGANIZATION COMMITTEE

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Final Call for Papers

ESSCIRC 2006

32nd European Solid-State Circuits Conference

18-22 September 2006
Montreux, Switzerland

Paper submission deadline: 7 April 2006
Electronic Submission

Organization committee at:



Technical co-sponsorship:



www.esscirt2006.com

ESSCIRC 2006 and ESSDERC 2006 are held jointly.

GENERAL SCOPE OF THE CONFERENCE

The aim of the ESSCIRC Conference is to provide an annual European forum for the presentation and discussion of recent advances in solid-state circuits and technologies. ESSCIRC and its sister conference ESSDERC, which deals with solid-state devices, are governed by a single Steering Committee. The increasing levels of integration for system-on-chip design made available by the advances in silicon technology are stimulating more than ever before the need for deeper interaction amongst technologists, device experts, and circuit and system designers.

ESSCIRC and ESSDERC will be jointly organized in 2006. While keeping separate Technical Program Committees, ESSCIRC and ESSDERC will share Plenary Keynote Presentations bridging both communities. Delegates registered for either Conference are encouraged to attend any of the scheduled parallel sessions.

The main themes for original contributions to be submitted to ESSDERC 2006 include (but are not limited to) the following:

➤ Analog Circuits

Analog circuits including: amplifiers; continuous-time & discrete-time filters; comparators; multipliers; voltage references; oscillators; power-control circuits; consumer electronics; non-linear analog circuits; op-amps; switched-capacitor circuits.

➤ RF Circuits

RF/IF/baseband circuits including: LNAs; mixers; IF amplifiers; power amplifiers; RF switches; power detectors; active antennas; modulators; demodulators; VCOs; frequency dividers; frequency synthesizers; phase-locked loops.

➤ Wireless Communication Circuits and Systems

Receivers/transmitters/transceivers for wireless systems including Bluetooth; WLAN; WPAN; WMAN; GPS; UWB; GSM/EDGE/CDMA/UMTS/3G base stations and handsets; TV/radio/satellite; DVB/DMB; MMDS; wireless sensor networks.

➤ Data Converters

Nyquist-rate and oversampled A/D and D/A converters; sample-and-hold circuits; A/D and D/A converter calibration circuits.

➤ SoC and SiP Integration (Digital and Mixed-Signal)

Digital and mixed-signal SoCs and SiPs for communication; data-storage; automotive; consumer; industrial and health applications; smart cards; techniques for SoC and SiP integration.

➤ Digital and Memory Circuits

Digital circuit techniques; I/O and interchip communication; reconfigurable digital circuits; clocking; memories; microprocessors; digital signal processors.

➤ Mixed Signal; High Voltage and High Power Circuits

Interface circuits; wireline and optical communication front-ends; power management circuits and systems; smart-power circuits; dc-dc converters.

➤ Imagers; Sensors and Microsystems

Imagers; image processing; circuits for sensors and actuators; circuits for MEMS; complete Micro- and Nanosystems.

To further emphasize the interactions between the device and circuits communities especially in the domain of emerging technologies, the conference will offer for the first time a joint session.

Joint ESSCIRC/ESSDERC Session on:

➤ Circuits and Devices Using Emerging Silicon Nanoelectronics

New digital; analog and mixed circuits using emerging devices such as multi-gate MOSFETs (Double-Gate MOSFETs; FinFETs; Triple-Gate MOSFETs); nanowires/nanotubes and quantum devices.

ESSCIRC / ESSDERC JOINT PLENARY TALKS

ESSCIRC and ESSDERC will share Plenary Sessions where distinguished invited speakers will discuss issues of interest for the attendees of both Conferences.

- **Georges Gielen** (KU Leuven)
Design Methodologies and Tools for Circuit Design in CMOS Nanometer Technologies
- **Karl McGoldrick** (Polymer Vision)
Mobile Friendly Rollable Displays
- **Henry Baltes** (ETH Zürich)
From Microsystems To Biosystems
- **Roger T. Howe** (Stanford University)
Integrated MEMS Resonator Technologies
- **Ken Uchida** (Toshiba Advanced LSI Laboratory)
Single Electron Transistors and Circuits for Future Low Power LSI
- **Tadahiro Kuroda** (Keio University)
Low-Power Inter-Chip Wireless Communication

ESSCIRC PLENARY TALKS

- **Rinaldo Castello** (University of Pavia)
Multimode Reconfigurable Wireless Terminals
- **Steve Kang** (University of California – Santa Cruz)
The Role of Bioelectronics for Quality of Life
- **Asad Abidi** (University of California – Los Angeles)
Evolution of the Software-Defined Radio Receiver

PAPER SUBMISSIONS

The 2006 ESSCIRC Conference will allow only electronic submission of the papers in PDF format. Prospective authors must submit their paper(s) via the conference website.

Papers must be submitted in the final format to be published in the Proceedings. They must not exceed four A4 pages with all illustrations and references included. The size of the PDF files submitted should not exceed 2 Mbytes. Manuscript guidelines (MS Word, LaTeX and PDF) as well as instructions on how to submit electronically are available on the conference website.

Conference Website
<http://www.esscirc2006.com>

All paper submissions must be received by Friday, 7 April 2006, 18h00 GMT.

After selection of papers, the authors will be informed of the decision of the Technical Program Committee by e-mail at the beginning of June 2006. At the same time, the complete program will be published on the internet.

The working language of the conference is English, which must also be used for all presentations and printed material.

REVIEW PROCESS

Papers submitted for review must clearly state:

- The purpose of the work
- How and to what extent it advances the state-of-the-art
- Specific results and their impact

The degree to which the paper deals with the above issues is fundamental to a successful review and selection of the paper. The most frequent cause of rejection of submitted papers is a lack of new results. Only work that has not been previously published at the time of the Conference will be considered. Submission of a paper for review and subsequent acceptance is considered by the Committee as a commitment that the work will not be placed in the public domain prior to the Conference.

BEST PAPER AWARDS

Papers presented at the Conference will be considered for the Best Paper Award and for the best "Young Scientist" Paper Award. The selection will be based on the judgment of the Conference participants and the award delivery will take place at ESSCIRC 2007.

SCHEDULE AT A GLANCE

Monday	18 September 2006	Tutorials / Registration
Tuesday	19 September 2006	Conference Opening Technical Sessions Welcome Reception
Wednesday	20 September 2006	Technical Sessions Conference Gala Dinner
Thursday	21 September 2006	Technical Sessions
Friday	22 September 2006	Workshops Excursions

KEY DATES

Submission deadline	7 April 2006
Notification of acceptance	1 June 2006
Advance registration deadline	15 July 2006

TUTORIALS AND WORKSHOPS

Tutorials will be organized on Monday, 18 September 2006, while Workshops will take place on Friday, 22 September 2006. Please consult the conference website for further details.